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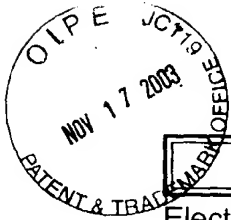
EFS ID: 50735
Application ID: 09961229
Title of Invention: High-Voltage Transistor With A
Multi-Layer Conduction Region
First Named Inventor: Vladimir Rumennik
Domestic/Foreign Application: Domestic Application
Filing Date: 2001-09-20
Effective Receipt Date: 2003-11-17
Submission Type: Information Disclosure
Statement
Filing Type:
Confirmation number: 4375
Attorney Docket Number: 03692.P007XD4



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Total Fees Authorized:

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**TRANSMITTAL**

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	High-Voltage Transistor With A Multi-Layer Conduction Region									
<div>Application Number: 09/961229 </div> <div>Date: 2001-09-20</div> <div>First Named Applicant: Vladimir Rumennik</div> <div>Confirmation Number: 4375</div> <div>Attorney Docket Number: 03692.P007XD4</div>										
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<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>Bradley J Berezna Registered Number: 33,474</td><td>BJB</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	Bradley J Berezna Registered Number: 33,474	BJB	Attorney		
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<table><tr><td>Documents being submitted</td><td>Files</td></tr><tr><td>us-ids</td><td>03692.P007XD4-usidst.xml</td></tr><tr><td></td><td>us-ids.dtd</td></tr><tr><td></td><td>us-ids.xsl</td></tr></table>			Documents being submitted	Files	us-ids	03692.P007XD4-usidst.xml		us-ids.dtd		us-ids.xsl
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Comments										



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	High-Voltage Transistor With A Multi-Layer Conduction Region																												
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<div>RECEIVED NOV 28 2003 ELECTRONIC CENTER 2800</div> <div>US Patent Documents</div> <div>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</div> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>6633065</td><td>2003-10-14</td><td>Rumennik et al.</td><td>B2</td><td></td><td></td></tr><tr><td></td><td>2</td><td>6639277</td><td>2003-10-28</td><td>Rumennik et al.</td><td>B2</td><td></td><td></td></tr></tbody></table> <div>Remarks</div> <div>Note: Remarks are not for responding to an office action.</div> <div>This information disclosure statement is being submitted concurrent with the filing of a Request for Continued Examination in the above-referenced application.</div> <div>Signature</div> <table border="1"><tr><td>Examiner Name</td><td>Date</td></tr><tr><td></td><td></td></tr></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	6633065	2003-10-14	Rumennik et al.	B2				2	6639277	2003-10-28	Rumennik et al.	B2			Examiner Name	Date		
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